EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	525	714/726.ccls.	USPAT	OR	OFF	2004/01/07 17:03
S3	753	714/726-729.ccls. and scan	USPAT	OR	OFF	2004/01/07 17:04
S4	623	714/726-729.ccls. and (integrated adj circuit or IC) and scan	USPAT	OR	OFF	2004/01/07 17:07
S5	577	714/726-729.ccls. and (integrated adj circuit or IC) and scan and (shift adj register of chain)	USPAT	OR	ON	2004/01/07 17:08
S8	182	714/726-729.ccls. and (integrated adj circuit or IC) with scan near3 (shift adj register of chain)	USPAT	OR	ON	2004/01/07 17:17
S9	13	714/726-729.ccls. and (integrated adj circuit or IC) with scan near3 (shift adj register of chain) and compression	USPAT	OR	ON	2004/01/07 17:18
S6	223	714/726-729.ccls. and (integrated adj circuit or IC) with scan with (shift adj register of chain)	USPAT	OR	ON	2004/01/07 17:27
S11	17	714/726-729.ccls. and (integrated adj circuit or IC) with scan with (shift adj register of chain) and compression	USPAT	OR	ON	2004/01/07 17:30
S13	20903	714/726-729.ccls. and (integrated adj circuit or IC) with scan nera3 (shift adj register of chain) and multiplexer	USPAT	OR	ON	2004/01/07 17:31
S12	129	714/726-729.ccls. and (integrated adj circuit or IC) with scan with (shift adj register of chain) and multiplexer	USPAT	OR	ON	2004/01/07 17:31
S16	8	("5132974" | "5260949" | "5467354" | "5526365" | "5535223" | "5592493" | "5604432" | "5633879").PN.	USPAT	OR	OFF	2004/01/07 17:38
S14	113	714/726-729.ccls. and (integrated adj circuit or IC) with scan near3 (shift adj register of chain) and multiplexer	USPAT	OR	ON	2004/01/07 17:41
S17	84	714/726-729.ccls. and (integrated adj circuit or IC) with scan adj chain and multiplexer	USPAT	OR	ON	2004/01/07 17:44

S19	18	("5323400" | "5369648" | "5596585" | "5612963" | "5925144" | "5930270" | "5983009" | "5983380" | "6012157" | "6041429" | "6061818" | "6178534" | "6314540" | "6327685" | "6393594" | "6438722" | "6442720" | "6442723").PN.	USPAT	OR	OFF	2004/01/08 11:20
S20	18	("5323400" | "5369648" | "5596585" | "5612963" | "5925144" | "5930270" | "5983009" | "5983380" | "6012157" | "6041429" | "6061818" | "6178534" | "6314540" | "6327685" | "6393594" | "6438722" | "6442720" | "6442723").PN.	USPAT	OR	OFF	2004/01/08 11:21
S23	3	("5323400" | "5369648" | "5596585" | "5612963" | "5925144" | "5930270" | "5983009" | "5983380" | "6012157" | "6041429" | "6061818" | "6178534" | "6314540" | "6327685" | "6393594" | "6438722" | "6442720" | "6442723").PN. and compression and "XOR"	USPAT	OR	OFF	2004/01/08 11:25
S21	5	("5323400" | "5369648" | "5596585" | "5612963" | "5925144" | "5930270" | "5983009" | "5983380" | "6012157" | "6041429" | "6061818" | "6178534" | "6314540" | "6327685" | "6393594" | "6438722" | "6442720" | "6442723").PN. and compression	USPAT	OR	OFF	2004/01/08 11:25
S18	8	714/726-729.ccls. and (integrated adj circuit or IC) with scan adj chain and multiple adj input adj shift adj register	USPAT	OR	ON	2004/01/08 11:30
S25	31	714/726-729.ccls. and (integrated adj circuit or IC) and scan adj chain and (multiple adj input adj shift adj register or MISR)	USPAT	OR	ON	2004/01/08 11:31
S24	11	714/726-729.ccls. and (integrated adj circuit or IC) and scan adj chain and multiple adj input adj shift adj register	USPAT	OR	ON	2004/01/08 11:31
S26	13	714/726-729.ccls. and (integrated adj circuit or IC) and scan adj chain and (multiple adj input adj shift adj register or MISR) and "XOR"	USPAT	OR	ON	2004/01/08 15:57

S27	15	714/726-733.ccls. and (integrated adj circuit or IC) and scan adj chain and pattern ADJ pattern	USPAT	OR	ON	2004/01/08 16:06
S29	15	714/726-733.ccls. and (integrated adj circuit or IC) and scan adj chain and pattern ADJ generator with memory	USPAT	OR	ON	2004/01/08 16:11
S30	1	714/726-733.ccls. and (integrated adj circuit or IC) and scan adj chain and pattern ADJ generator with multiplexer with memory	USPAT	OR	ON	2004/01/08 16:12
S31	7	714/726-733.ccls. and (integrated adj circuit or IC) and scan adj chain and pattern ADJ generator with multiplexer	USPAT	OR	ON	2004/01/08 16:14
S32	31	"714"/$.ccls. and (integrated adj circuit or IC) and scan adj chain with pattern ADJ generator	USPAT	OR	ON	2004/01/08 16:16
S37	27	714/718.ccls. and (integrated adj circuit or IC) and scan ADJ chain	USPAT	OR	ON	2004/01/08 16:22
S36	6	714/718.ccls. and (integrated adj circuit or IC) and pattern ADJ generator and scan ADJ chain	USPAT	OR	ON	2004/01/08 16:22
S35	60	714/718.ccls. and (integrated adj circuit or IC) and pattern ADJ generator	USPAT	OR	ON	2004/01/09 09:03
S40	26	714/718.ccls. and (integrated adj circuit or IC) and pattern near3 checkerboard	USPAT	OR	ON	2004/01/09 09:05
S41	3	714/718.ccls. and (integrated adj circuit or IC) and pattern near3 checkerboard and combinational	USPAT	OR	ON	2004/01/09 09:07
S42	3	714/718.ccls. and (integrated adj circuit or IC) and pattern near3 checkerboard and combinational adj logic	USPAT	OR	ON	2004/01/09 09:08
S2	838	714/726-729.ccls.	USPAT	OR	OFF	2004/11/15 10:32
S43	3	714/718.ccls. and (integrated adj circuit or IC) and pattern near3 checkerboard and combinational adj logic	USPAT	OR	ON	2004/11/15 10:34
S44	2	US-6263461-$.DID. OR US-5557619-$.DID.	USPAT	OR	OFF	2004/11/15 10:39
S39	1	714/718.ccls. and (integrated adj circuit or IC) and pattern ADJ generator with checkerboard	USPAT	OR	ON	2004/11/15 10:46

S47	50	714/718-720.ccls. and predetermined adj pattern	USPAT	OR	ON	2004/11/15 10:48
S48	32	714/718-720.ccls. and predetermined adj pattern with memory	USPAT	OR	ON	2004/11/15 11:22
S51	3	714/718-720.ccls. and predetermined adj pattern with count$3	USPAT	OR	ON	2004/11/15 11:24
S50	3	714/718-720.ccls. and predetermined adj pattern with memory with count$3	USPAT	OR	ON	2004/11/15 11:24
S54	1	US-6263461-$.DID.	USPAT	OR	OFF	2005/07/12 12:17
S15	1	("5812561").PN.	USPAT; USOCR	OR	OFF	2005/07/12 12:17
S55	3	(LAUGA adj CHRISTOPHE)".in. "	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/07/12 15:25
S28	92	714/726-733.ccls. and (integrated adj circuit or IC) and scan adj chain and pattern ADJ generator	USPAT	OR	ON	2005/07/12 15:25
S57	1	(LAUGA adj CHRISTOPHE).in. and (combinational with logic).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/07/12 15:27
S58	1	("6154715").PN.	USPAT	OR	OFF	2005/07/16 10:51
S38	1	("6263461").PN.	USPAT; USOCR	OR	OFF	2005/07/25 10:00
S59	2	US-6263461-$.DID. OR US-5557619-$.DID.	USPAT	OR	OFF	2005/07/25 10:02
S63	116	memory with logic with test$ with simultaneous$3	USPAT	OR	OFF	2005/07/25 10:04
S61	21	memory with logic with test$ with synchronous	USPAT	OR	OFF	2005/07/25 10:04
S65	14	memory with logic with test$ with simultaneous$3 same scan	USPAT	OR	OFF	2005/07/25 10:05
S64	143766	memory with logic with test$ with simultaneous$3 sane scan	USPAT	OR	OFF	2005/07/25 10:05
S66	35	memory near3 logic near simultaneous$3	USPAT	OR	OFF	2005/07/25 10:06
S70	4	"714"/$.ccls. and memory with logic with scan with test$3 with simultaneous$3	USPAT	OR	OFF	2005/07/25 10:08
S69	5042	"714"/$.ccls. and memory with logic witrh scan with test$3 with simultaneous$3	USPAT	OR	OFF	2005/07/25 10:08

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S68	63	"714"/$.ccls. and memory with logic with test$3 with simultaneous$3	USPAT	OR	OFF	2005/07/25 10:09
S71	7	"714"/$.ccls. and memory near3 logic near3 simultaneous$3	USPAT	OR	OFF	2005/07/25 10:10
S74	6	"714"/$.ccls. and memory near3 logic near3 scan near3 test$3	USPAT	OR	ON	2005/07/25 10:13
S75	162	"714"/$.ccls. and memory with logic with simultaneous$3	USPAT	OR	ON	2005/07/25 10:14
S72	9	"714"/$.ccls. and memory near3 logic near3 simultaneous$3	USPAT	OR	ON	2005/07/25 10:14
S76	2	memory with logic with test$3 with simultaneous$3 and scan with chain	USPAT	OR	ON	2005/07/25 10:36
S78	328	"714"/718-738.ccls. and memory near3 logic near3 test$3	USPAT	OR	ON	2005/07/25 10:37
S73	378	"714"/$.ccls. and memory near3 logic near3 test$3	USPAT	OR	ON	2005/07/25 10:37
S79	6	"714"/718-738.ccls. and memory near3 logic near3 scan near3 test$3	USPAT	OR	ON	2005/07/25 10:39
S80	562	"714"/718-738.ccls. and International adj Business adju Macines.as. and memory near3 logic near3 scan near3 test$3	USPAT	OR	ON	2005/07/25 10:40
S81	8	"714"/718-738.ccls. and International adj Business adj Machines.as. and memory with logic with simultaneous$3	USPAT	OR	ON	2005/07/25 10:43
S83	1065	"714"/718-738.ccls. and memory with logic with test$3	USPAT	OR	ON	2005/07/25 10:48
S82	94	"714"/718-738.ccls. and memory with logic with simultaneous$3	USPAT	OR	ON	2005/07/25 10:48
S85	136	"714"/718-738.ccls. and memory with scan with logic with test$3	USPAT	OR	ON	2005/07/25 13:56
S7	17	714/726-729.ccls. and (integrated adj circuit or IC) with scan with (shift adj register of chain) and compression	USPAT	OR	ON	2005/07/25 18:45
S87	59	714/726-738.ccls. and address with data with generator and BIST	USPAT	OR	ON	2005/07/25 18:46
S86	220	714/726-738.ccls. and address with data with generator	USPAT	OR	ON	2005/07/25 18:46
S89	2	714/726-738.ccls. and address near2 data near3 generator with counter and BIST	USPAT	OR	ON	2005/07/25 18:50
S88	21	714/726-738.ccls. and address near2 data near3 generator and BIST	USPAT	OR	ON	2005/07/25 18:50

S92	7	"714"/$.ccls. and combinat$5 adj logic near3 memory adj array	USPAT	OR	ON	2005/07/26 17:37
S91	6	"714"/$.ccls. and combinat$5 adj logic near3 memory adj array	USPAT	OR	OFF	2005/07/26 17:37
S90	10	combinat$5 adj logic with memory with simulat$3	USPAT	OR	OFF	2005/07/26 17:37
S56	3	(LAUGA adj CHRISTOPHE).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/07/27 08:31
S97	1	S93 and S96	USPAT	OR	OFF	2005/07/27 08:34
S96	187	(memory with combinat$5 adj logic).clm.	USPAT	OR	ON	2005/07/27 08:34
S95	1307	memory with combinat$5 adj logic	USPAT	OR	ON	2005/07/27 08:34
S94	1245	memory with combinat$5 adj logic	USPAT	OR	OFF	2005/07/27 08:34
S67	115	memory with logic with test$3 with simultaneous$3	USPAT	OR	OFF	2006/06/15 10:11
S100	11	"714"/$.ccls. and memory near3 logic near3 test$3 with simultaneous$3 and @ad<"20010914"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/15 10:14
S99	71	"714"/$.ccls. and memory with logic with test$3 with simultaneous$3 and @ad<"20010914"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/15 10:15
S102	21	"714"/$.ccls. and memory near3 logic with test$3 with simultaneous$3 and @ad<"20010914"	US-PGPUB; USPAT	OR	ON	2006/06/15 10:18
S101	24	"714"/$.ccls. and memory near3 logic with test$3 with simultaneous$3 and @ad<"20010914"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/15 10:18
S105	41	"714"/$.ccls. and memory near logic near test$3 and @ad<"20010914"	US-PGPUB; USPAT	OR	ON	2006/06/15 10:19
S103	641	"714"/$.ccls. and memory near3 logic with test$3 and @ad<"20010914"	US-PGPUB; USPAT	OR	ON	2006/06/15 10:19
S104	385	"714"/$.ccls. and memory near3 logic near3 test$3 and @ad<"20010914"	US-PGPUB; USPAT	OR	ON	2006/06/15 10:22
S106	4	"714"/$.ccls. and memory near3 combinational adj logic near3 test$3 and @ad<"20010914"	US-PGPUB; USPAT	OR	ON	2006/06/15 10:25

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S10 8	28	"714"/$.ccls. and memory near3 logic near3 test$3 with scan and @ad<"20010914"	US-PGPUB; USPAT	OR	ON	2006/06/15 10:26
S10 7	176	"714"/$.ccls. and memory near3 logic near3 test$3 and scan and @ad<"20010914"	US-PGPUB; USPAT	OR	ON	2006/06/15 10:26
S98	73	"714"/$.ccls. and memory with logic with test$3 with simultaneous$3	USPAT	OR	ON	2006/06/15 10:28
S45	1	"20030056161"	US-PGPUB; USPAT	OR	OFF	2006/06/15 18:50
S11 1	1	"20040250183"	US-PGPUB	OR	OFF	2006/06/15 18:51
S11 2	1681	714/718-720.ccls.	USPAT	OR	ON	2006/06/16 09:55
S52	3	714/718-720.ccls. and predetermined adj pattern with count$3	USPAT	OR	ON	2006/06/16 09:55
S11 4	153	S112 and memory near3 logic near3 test$3 and @ad<"20010914"	USPAT	OR	ON	2006/06/16 09:57
S11 3	463	S112 and memory with logic with test$3 and @ad<"20010914"	USPAT	OR	ON	2006/06/16 09:57
S10 9	19	"714"/$.ccls. and memory with logic with test$3 with simultaneous$3 and scan and @ad<"20010914"	USPAT	OR	ON	2006/06/16 09:57
S11 7	3	(LAUGA adj CHRISTOPHE).in. and (memory and test$3)".clm"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:00
S11 6	3	(LAUGA adj CHRISTOPHE).in. and (memory and test$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:00
S11 5	3	(LAUGA adj CHRISTOPHE).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/16 10:00
S11 8	3	(LAUGA adj CHRISTOPHE).in. and (memory and test$3)".clm."	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:01
S12 0	254	STMICROELECTRONICS ADJ LIMITED.as.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:02

S11 9	3	(LAUGA adj CHRISTOPHE).in. and (memory and logic and test$3)". clm."	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:02
S93	201	STMICROELECTRONICS ADJ LIMITED.as.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/06/16 10:02
S12 3	2	S120 and (memory and combinational adj logic and test$3)".clm."	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:03
S12 1	60	S120 and (memory and logic and test$3)".clm."	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/06/16 10:03